

## List of Publications:

1. **B. De**, “Studies on the Performance of Different kinds of Voltage Controlled Oscillator Circuits Using NE 555 Timer IC”, *MANAN : An Academic Journal of ABSGCTA*, vol. II, No. 02, pp 75 – 81 , July, 2015.
2. **B. De**, “On the performance of an Op-Amp based amplitude Limiter Circuit”, *MANAN : An Academic Journal of ABSGCTA*, vol. II, No. 01, pp 29 - 35 , January, 2015.
3. **B. De**, “Destination Red Planet: History of Different Mission, and India’s Space Race”, *Communique*, vol. 09, No. 01, pp. 59-68, March, 2015.
4. **B. De**, “Effect of Phase Detector Transfer Characteristics on the Tracking performance of Conventional PLL”, Purulia JK College, vol. VII , pp. 150 - 156 , July, 2014
5. **B. De**, “A New Look into the Performance of Wien Bridge Oscillator Circuit”, *MANAN : An Academic Journal of ABSGCTA*, vol. 01, No. 02, pp 25 - 34 , July, 2014.
6. **B. De**, “An Experimental Study on Phase Measurement Using Different Techniques”, *MANAN : An Academic Journal of ABSGCTA*, vol. 1, No. 1, pp 01- 05 , January, 2014.
7. **B. De**, “Environmental Hazards of Cell Phone: An Overview”, *Journal of Integrated research & Development*, vol.3, No. 3, pp 25-30, December 2013.
8. **B.De & B.C.Sarkar**, “Performance of non-linear amplifier based Phase locked loop systems in presence of channel perturbations”, *Indian Journal of Pure and Applied Physics*, vol. 47, pp 310-316, April 2009.
9. **B.De & B.C.Sarkar**, “A novel quick response charge pump Phase locked loop”, *Indian Journal of Engineering and Material Sciences*, vol. 15, pp 473-482 ,December 2008.
10. **B.De & B.C.Sarkar**, “Nonlinear dynamics of a nonlinear amplifier based delayed PLL incorporating additional phase modulator”, *International Journal of Electronics (UK)*, vol. 95, No. 9, pp 939-949, September 2008.
11. **B.C.Sarkar & B.De**, “A new look into the PLL response degradation due to VCO nonlinearity and its compensation technique”, *Indian Journal of Pure and Applied Physics*, vol. 38, pp 734-740, Oct’ 2000.
12. **B.C.Sarkar & B.De**, “On the performance of conventional charge pump PLLs incorporating different kinds of nonlinear amplifiers”, *IETE Journal of Research*, vol. 46, No. 4, pp 221-225, July-Aug’ 2000.
13. **B.De**, “On the performance of a steered frequency PLL”, *Proceedings of INCURSI 99: Frontiers of Electronics, Communication and Photonics*, pp 85-87, Feb’1999.

14. **B.C.Sarkar & B.De**, “Effect of additional loop time delay on the performance of nonlinear amplifier based PLL with and without phase modulator”, *International Journal of Electronics (UK)*, vol. 84, No. 4, pp 321-333, April 1998.
15. **B.De & B.C.Sarkar**, “Effect of co-channel tone interference on the performance of phase locked loops incorporating a class of nonlinear amplifiers”, *Indian Journal of Engineering and Material Sciences*, vol. 5, pp 55-57, April’ 1998.
16. **B.C.Sarkar & B.De**, “On the performance of a non-linear amplifier-based charge pump phase locked loop”, *Proceedings of International conference on computer and devices for communication (IEEE)*, Jan’ 1998.
17. **B.C.Sarkar, B.De & S.Sarkar**, “Structure and performance of a new data clock time recovery phase locked loop”, *Indian Journal of Engineering and Material Sciences*, vol. 3, pp 185-190, Oct’ 1996.
18. **B.De & B.C.Sarkar**, “Effect of using a nonlinear amplifier in a bit synchronizer”, *Proceedings of 3<sup>rd</sup> state Science Congress*, March’ 1996.